

Figure 1a

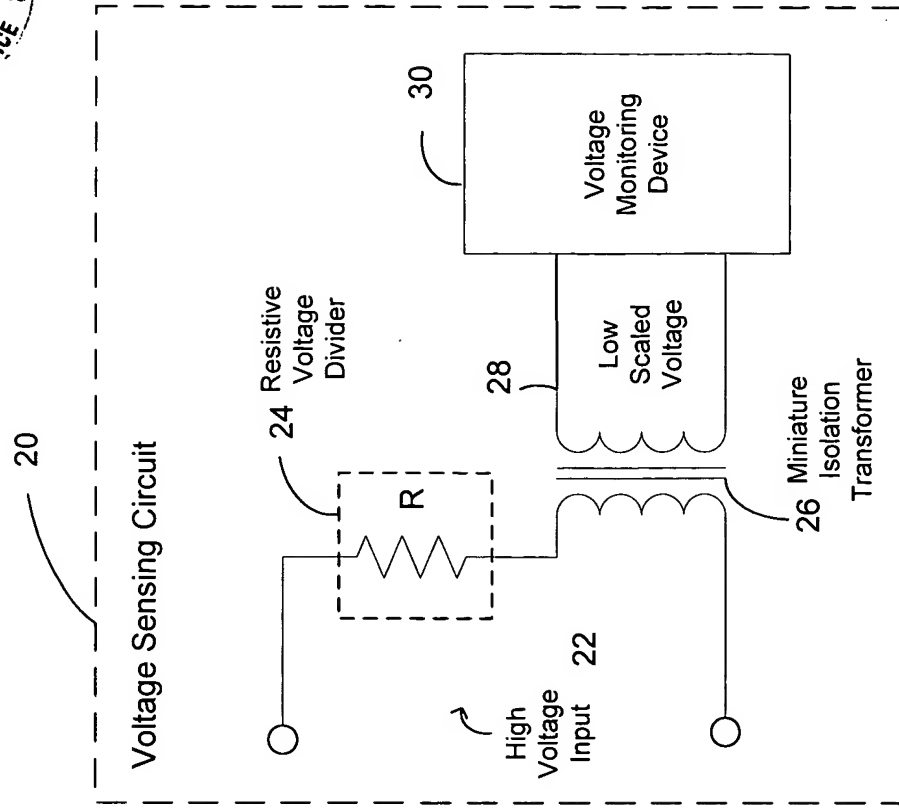


Figure 1b

PRIOR ART

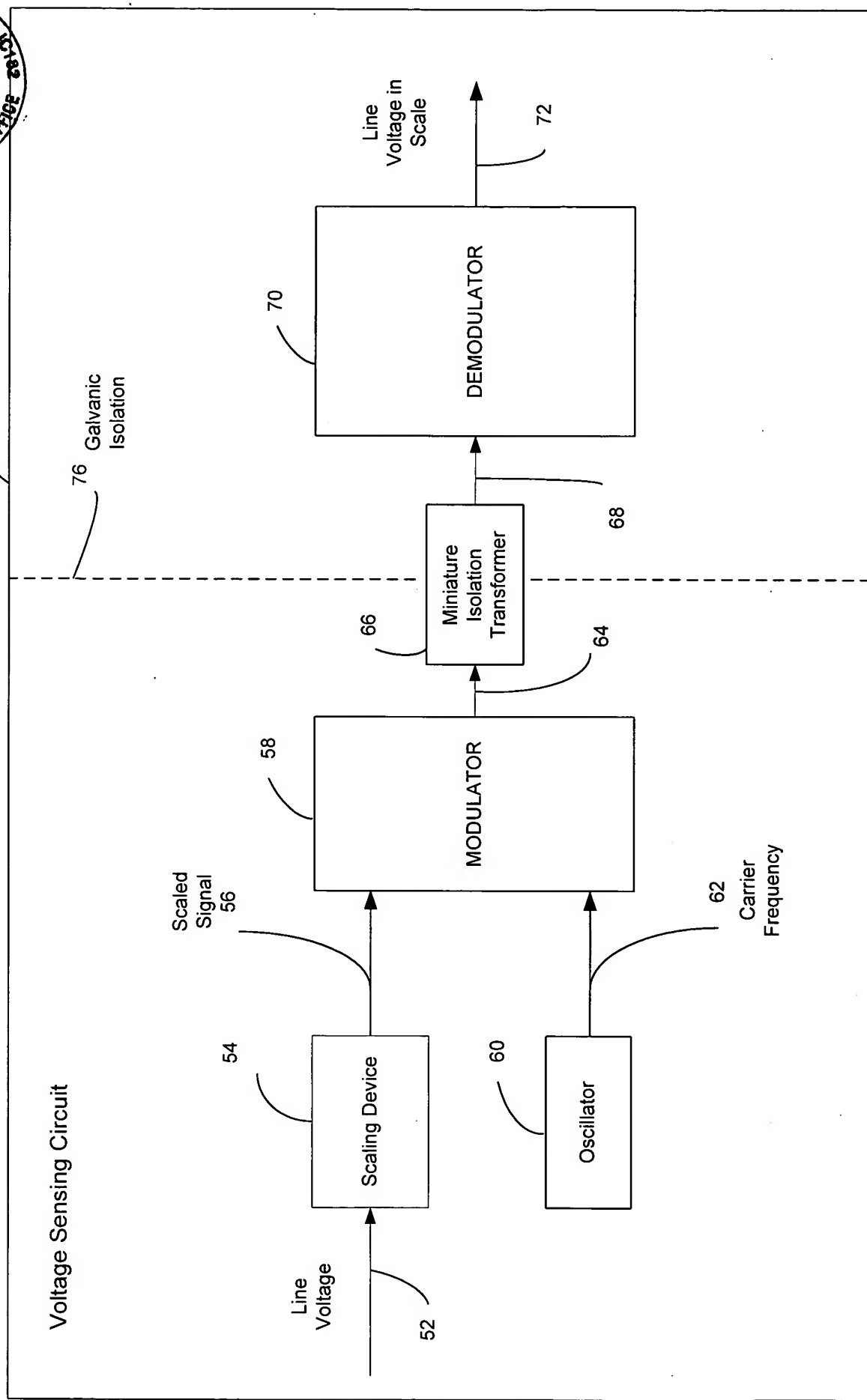


Figure 2

100

Primary Side

Secondary Side

104

102

106

101

103

Scaling Device

High Impedance Voltage Divider

Line Voltage

V_{in}

116

118

Buffer

High Impedance Input

Low Impedance Output

124

126

128

129

DC Bias

130

132

134

136

138

140

142

144

146

148

150

152

154

156

160

Output Line Voltage in scale

DEMOMULATOR

MODULATOR

Galvanic Isolation

Miniature Isolation Transformer

114

112

110

108

106

104

102

100

98

96

94

92

90

88

86

84

82

80

78

76

74

72

70

68

66

64

62

60

58

56

54

52

50

48

46

44

42

40

38

36

34

32

30

28

26

24

22

20

18

16

14

12

10

8

6

4

2

0

100

200

300

400

500

600

700

800

900

1000

1100

1200

1300

1400

1500

1600

1700

1800

1900

2000

2100

2200

2300

2400

2500

2600

2700

2800

2900

3000

3100

3200

3300

3400

3500

3600

3700

3800

3900

4000

4100

4200

4300

4400

4500

4600

4700

4800

4900

5000

5100

5200

5300

5400

5500

5600

5700

5800

5900

6000

6100

6200

6300

6400

6500

6600

6700

6800

6900

7000

7100

7200

7300

7400

7500

7600

7700

7800

7900

8000

8100

8200

8300

8400

8500

8600

8700

8800

8900

9000

9100

9200

9300

9400

9500

9600

9700

9800

9900

10000

10100

10200

10300

10400

10500

10600

10700

10800

10900

11000

11100

11200

11300

11400

11500

11600

11700

11800

11900

12000

12100

12200

12300

12400

12500

12600

12700

12800

12900

13000

13100

13200

13300

13400

13500

13600

13700

13800

13900

14000

14100

14200

14300

14400

14500

14600

14700

14800

14900

15000

15100

15200

15300

15400

15500

15600

15700

15800

15900

16000

16100

16200

16300

16400

16500

16600

16700

16800

16900

17000

17100

17200

17300

17400

17500

17600

17700

17800

17900

18000

18100

18200

18300

18400

18500

18600

18700

18800

18900

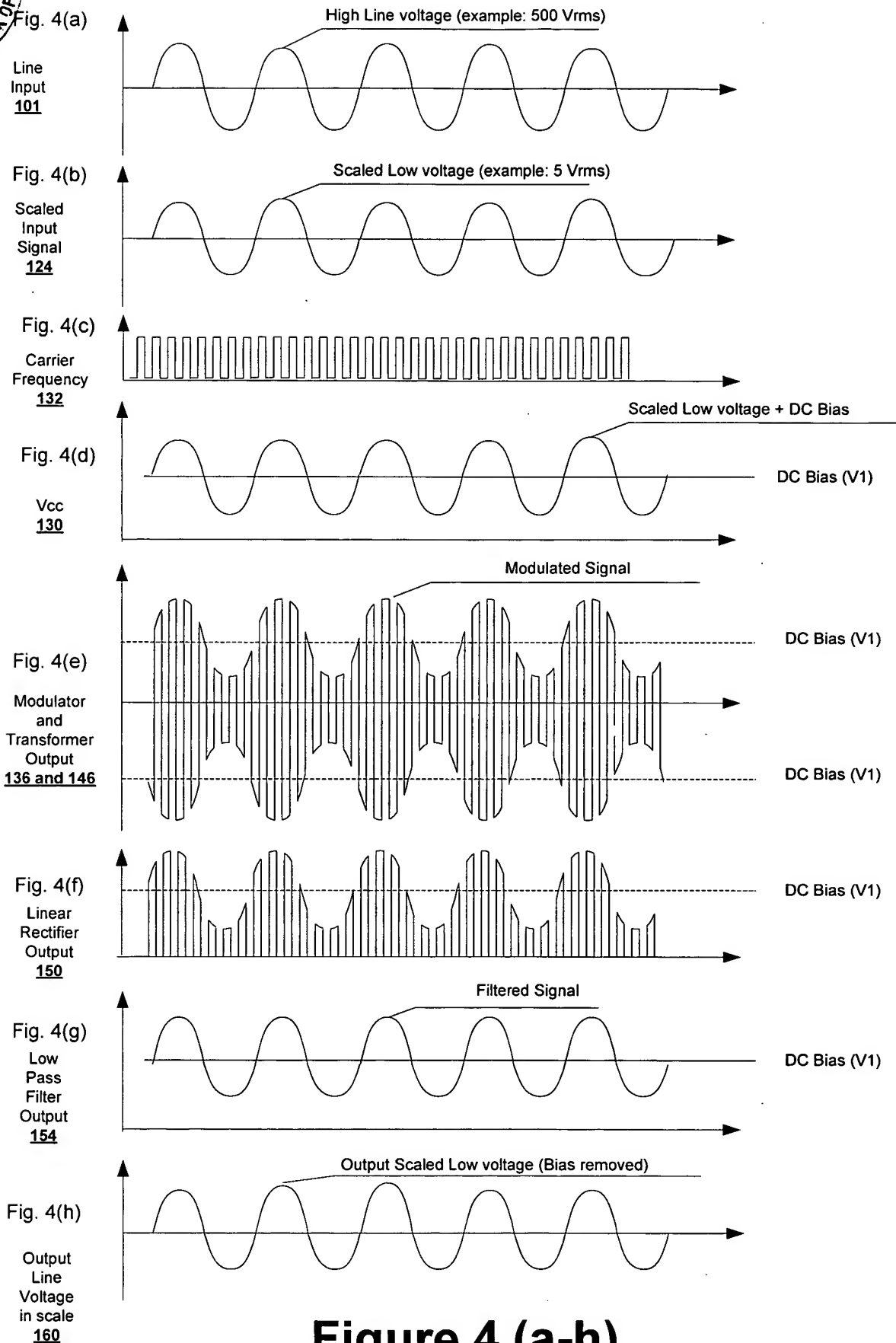


Figure 4 (a-h)

200

Galvanic Isolation
76

Voltage Sensing Circuit

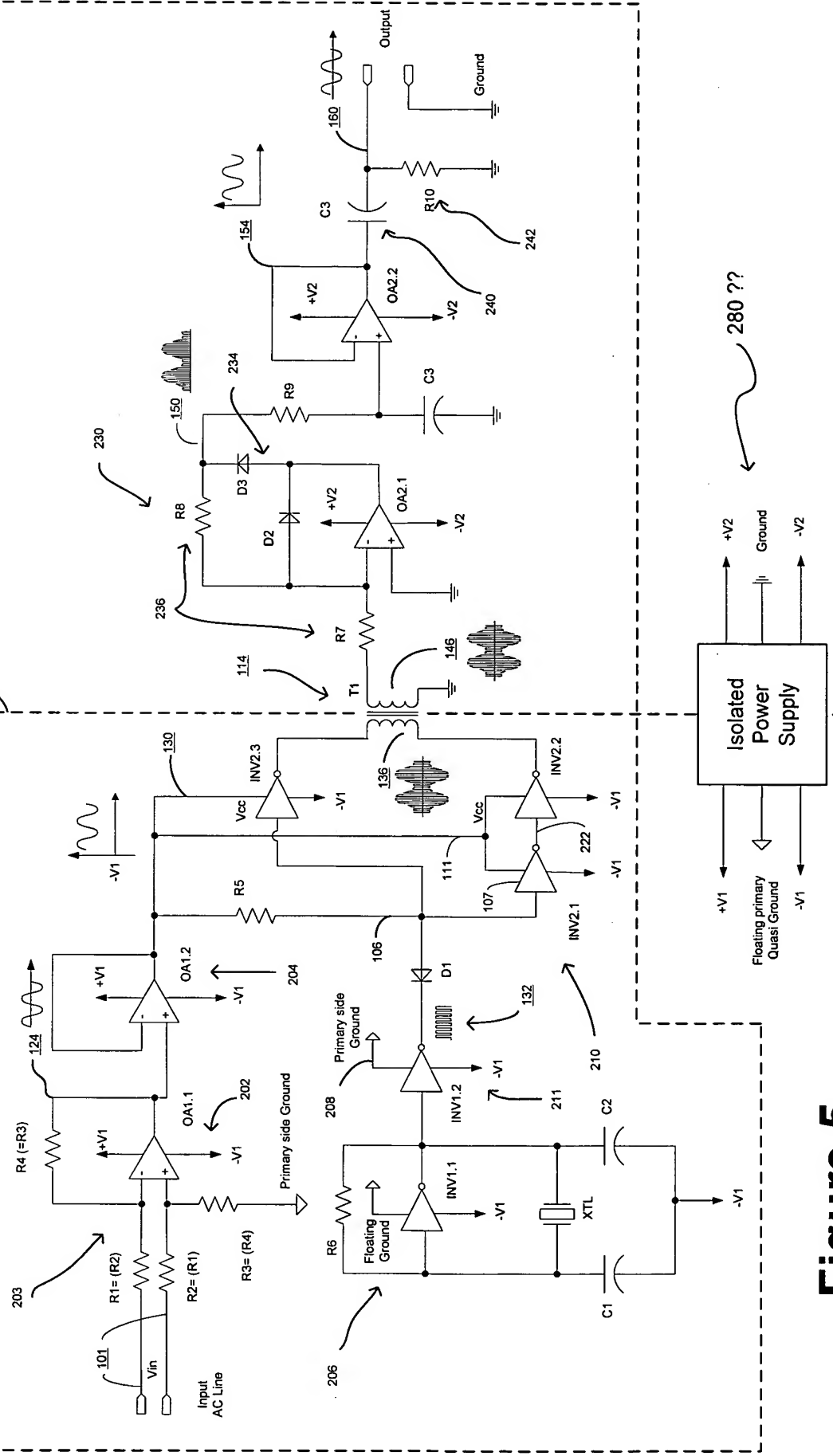


Figure 5

260

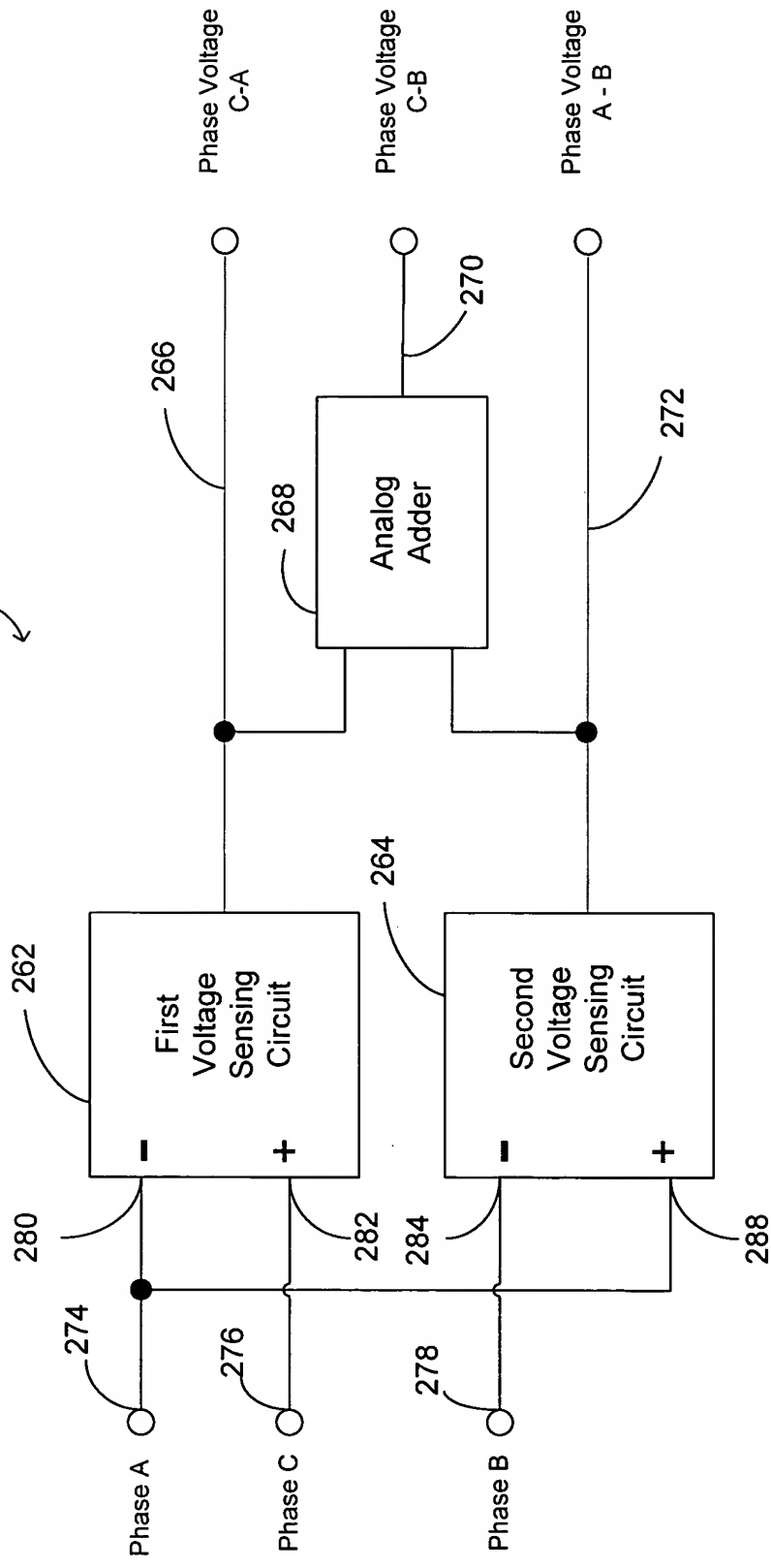


Figure 6

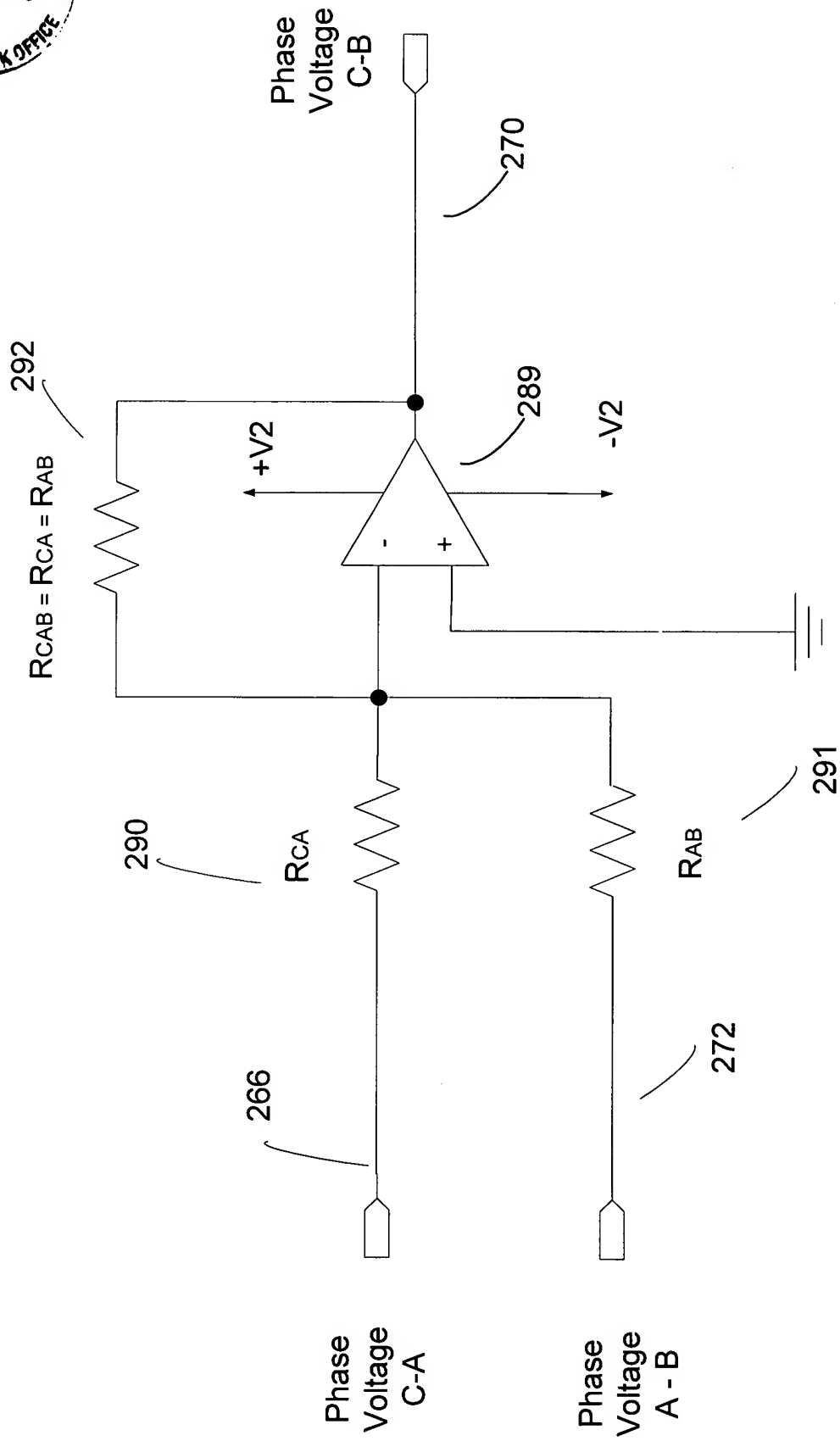


Figure 7